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I hereby certify that this correspondence is being transmitted to  
Group Art Unit 2813, 703-872-8308, addressed to:  
Commissioner for Patents, P.O. Box 1450, Alexandria, VA  
22313-1450.

Date: February 10, 2004

Sonia V. McVean  
Sonia V. McVean

**RESPONSE UNDER 37 C.F.R. § 1.116  
EXPEDITED PROCEDURE  
GROUP ART UNIT 2813****PATENT  
36856.648****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>Applicant:</b> Masahiko KAWASE et al.	<b>Art Unit:</b> 2813
<b>Serial No.:</b> 10/090,806	
<b>Filed:</b> March 6, 2002	<b>Examiner:</b> N. Berezny
<b>Title:</b> A CHIP-TYPE COMPOSITE ELECTRONIC COMPONENT AND MANUFACTURING METHOD THEREOF	

**REQUEST FOR RECONSIDERATION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated November 18, 2003, please reconsider the  
above identified application in view of the following remarks.

**A listing of the claims** begins on page 2 of this paper for the convenience of the  
Examiner. No amendments are being made by this listing.

**Remarks/Arguments** begin on page 6 of this paper.

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